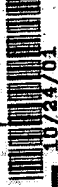


JC978 U.S. PTO  
10/029476



U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10029476	FILING DATE 10/24/2001	CLASS 716	SUBCLASS 11	GAU 2825	EXAMINER <i>Garbours</i>
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**\*\*APPLICANTS:** Lampaert Koen; Brotman Andy; Miliuzzi Paolo; Singh Paramjit;  
Matloubian Mishel; Bhattacharyya Bijan;

**\*\*CONTINUING DATA VERIFIED:** *W*

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**\*\* FOREIGN APPLICATIONS VERIFIED:** *N*

PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Verified and Acknowledged Examiners's initials <i>NSAN</i>		ATTORNEY DOCKET NO 01CON214P
TITLE : Method and system for predictive multi-component circuit layout generation with reduced design cycle		

U.S. DEPT. OF COMM. / PAT. & TM. PTO-436L/Rev. 12-94

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>Application Examiner</b>	
		<b>PREPARED FOR ISSUE</b>	
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